

PEBBLE

CPU : INTEL DIAMONDVILLE
Chip Set : INTEL 945GSE
Remarks :

Model Name : PEBBLE
PCB Part No : BA41-01066A (GCE)
BA41-01067A (NY)
BA41-XXXXXX (HS)
Dev. Step : PR
Revision : 0.1
T.R. Date : 2009.03.02

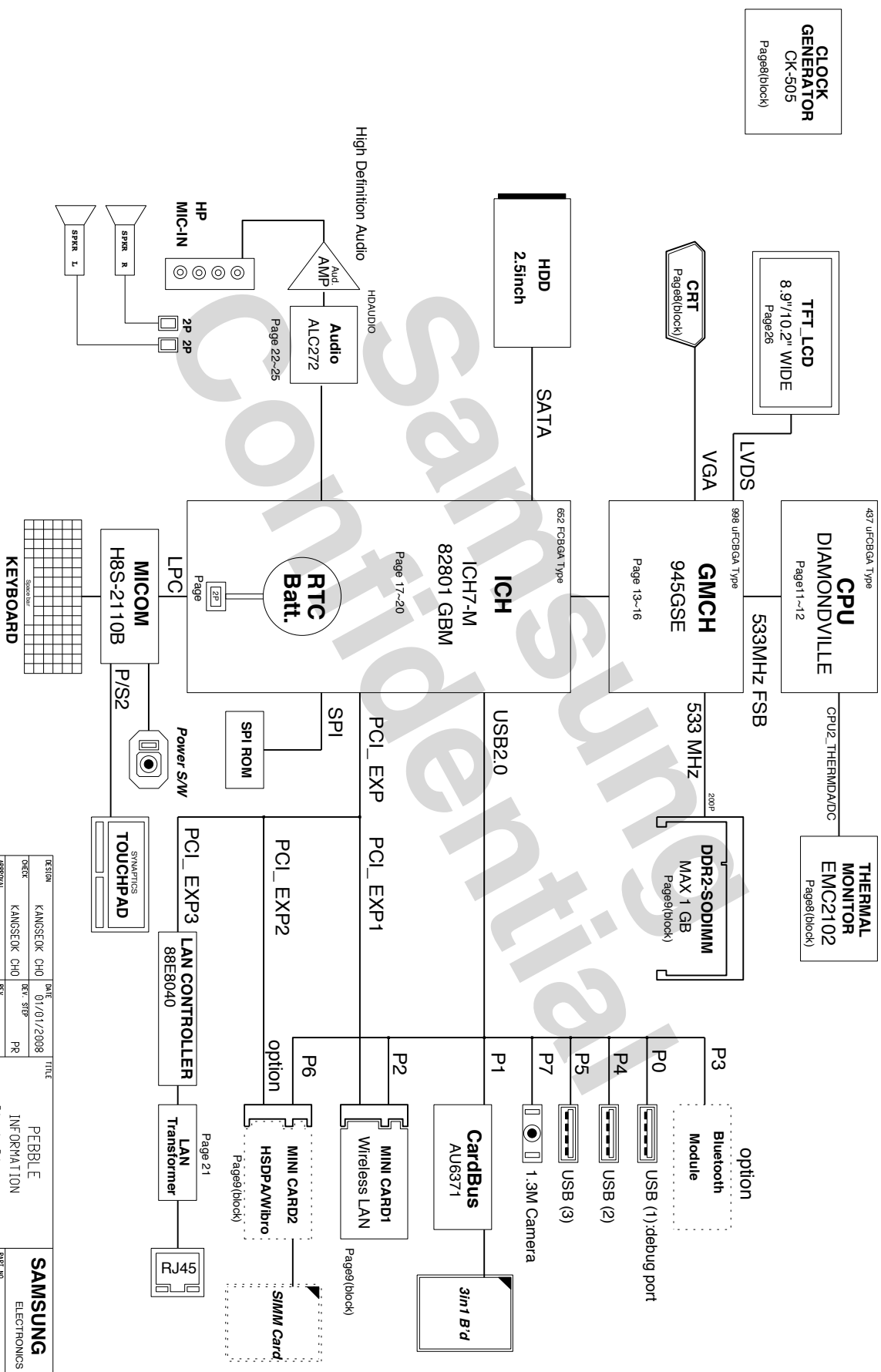
DRAW	CHECK	APPROVAL

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DESIGN	DATE	TITLE	SAMSUNG
DESIGN	01/01/2008	PEBBLE	ELECTRONICS
CHECK	REV. STEP	COVER	
APPROVAL	PR	COVER	
REV	0.2		
ROUTE CODE	JC K00		
LAST EDIT	February 28, 2009 16:53:29 PM	PAGE	1 OF 45

OPERATION BLOCK DIAGRAM



BOARD INFORMATION

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI Devices			
Devices	IDSEL#	REQ/GNT#	Interrupts
USB HID to PCI AD24(internal) USB to PCI AD24(internal) Internal MAC AD24(internal)	AD24(internal) AD24(internal) AD24(internal) AD24(internal)	Programmable	

I ² C / SMB Address			
Devices	Address	Hex	Bus
IC17M CK505M(Clock Generator)	1101 001X	D2h	SMBUS Master
SD1MM0	1010 001X	A0h	Clock, Unused Clock Output Disable
CPU Thermal Sensor	0111 101X	7Ah	Thermal Sensor

USB PORT Assign			
Port Number	ASSIGNED TO	Port Number	ASSIGNED TO
UHCI_0 0	USB PORT	UHCI_2 4	USB PORT
UHCI_1 1	3-IN-1	UHCI_3 5	USB PORT
UHCI_1 2	Wireless LAN		HSDPA
	Bluetooth		Camera

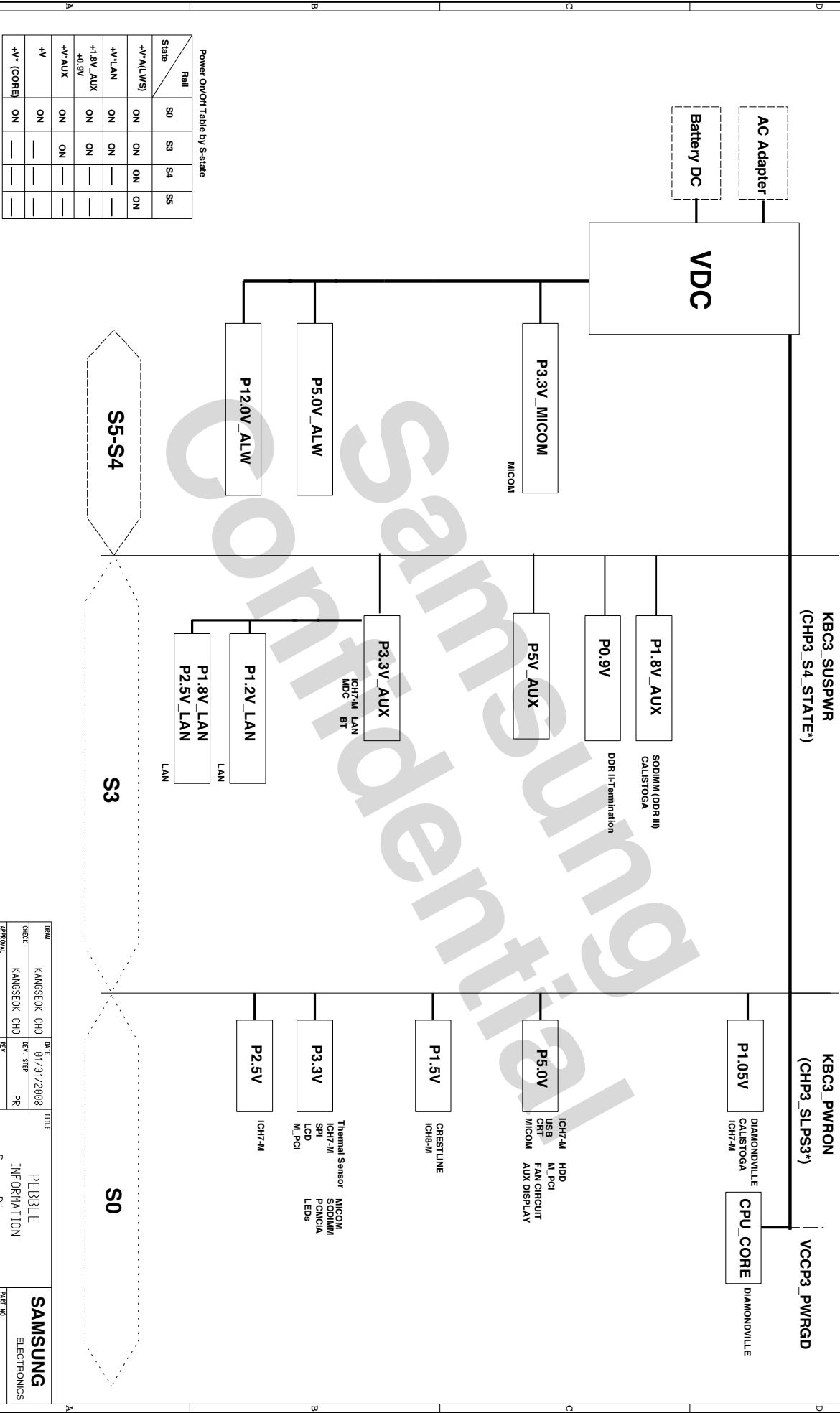
PCI Express Assign	
Port Number	ASSIGNED TO
1	Mini Card 1 (Wireless LAN)
2	Mini Card 2 (HSDPA/WiBro)
3	LOM

Voltage Rails	
Power Rail	Descriptions
PRTC_BAT	3.3V (can drop to 2.0V min. in G3 state) supply for the RTC well.
VDC	Primary DC system power supply (9 to 19V)
P1.65V_VCCP	VTT for CPU, Calistoga & IC17M
P1.5V_VCCM	1.5V power rail (off in S3-S5)
P1.5V_VCCM	1.5V switched power rail (off in S3-S5)
P1.8V_AUX	1.8V power rail for DDR (off in S4-S5)
P0.9V_AUX	0.9V power rail for DDR (off in S4-S5)
P3.3V_AUX	3.3V power rail (off in S4-S5)
P3.3V_AUX	3.3V power rail (off in S4-S5)
P5V	5.0V switched power rail (off in S3-S5)
P3.3V	3.3V switched power rail (off in S3-S5)
CPU_CORE	Core voltage for Atom CPU

Multi-Assy options	
Options	Meaning
nostut only_N270 only_N260	This material is not mounted If CPU is N270, this material is mounted If CPU is N260, this material is mounted

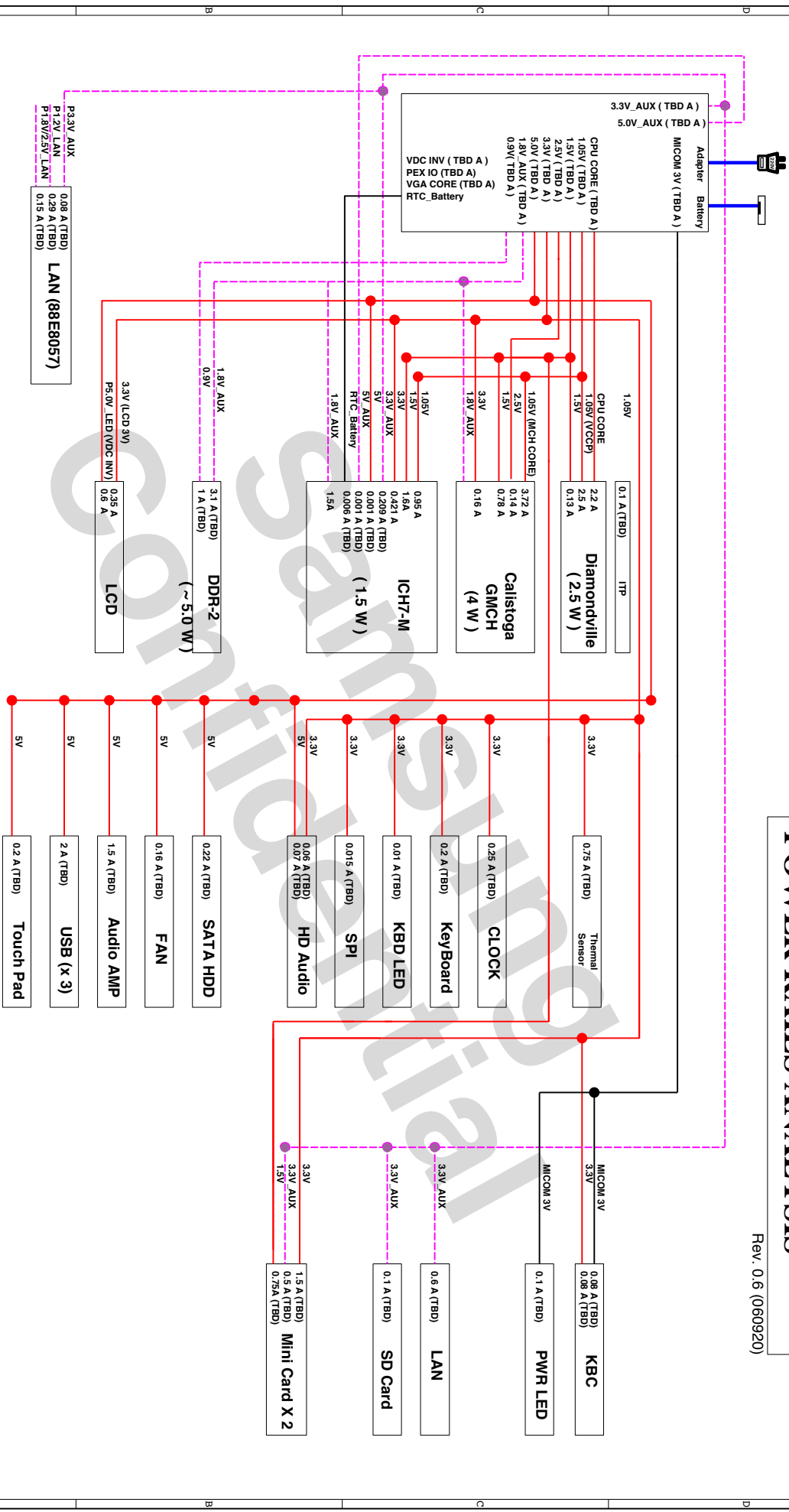
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APPROVAL	JC KOO	REV	0.2		
ROUTE CODE		LAST EDIT	February 28, 2009 16:53:29 PM	PAGE	3 OF 45

POWER DIAGRAM
Rev 0.1



POWER RAILS ANALYSIS

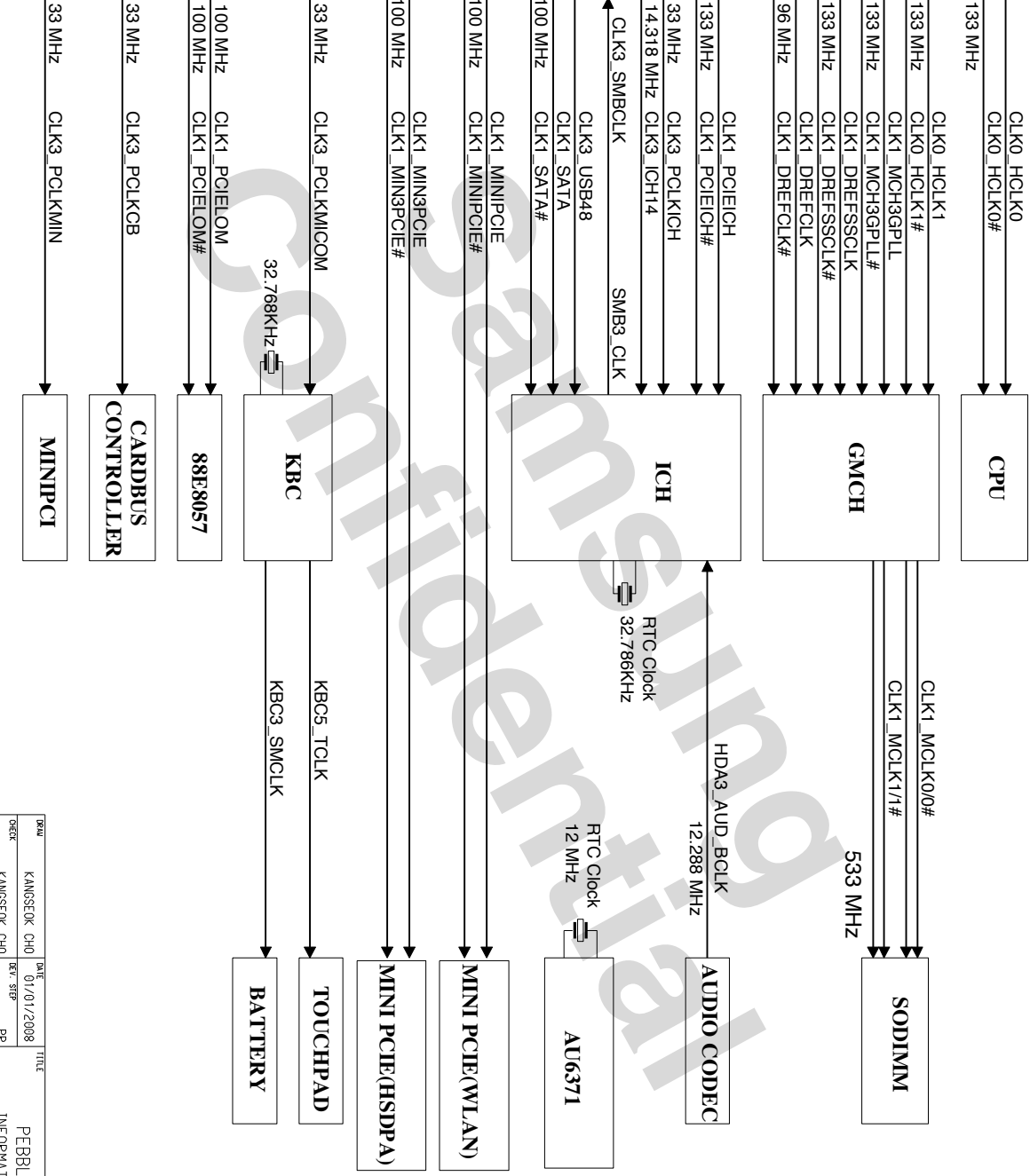
Rev. 0.6 (060920)





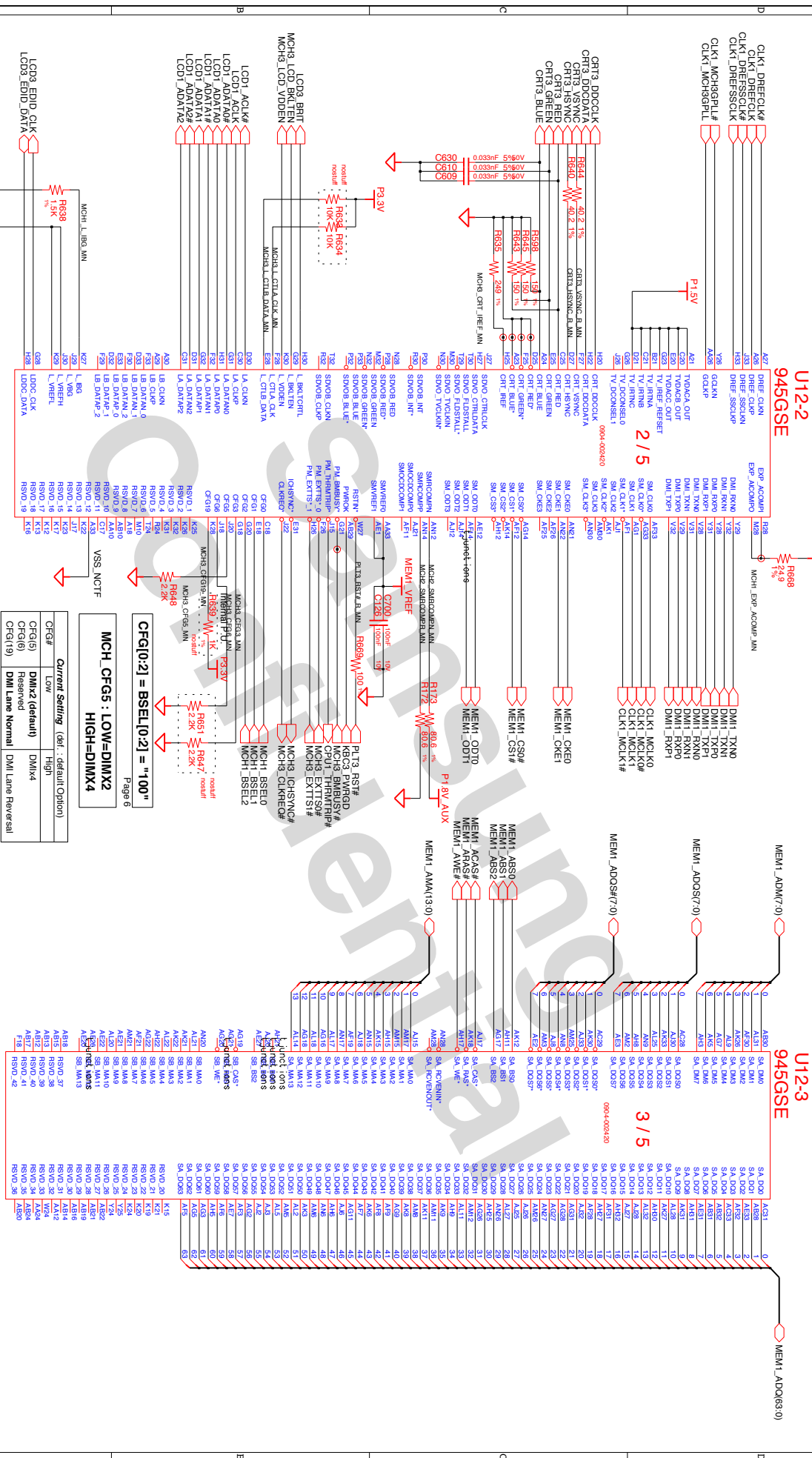
CLOCK DISTRIBUTION

CLOCK GENERATOR
CK-505M
IDTCV179BNLG



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0002	01/01/2008	Clock Distribution	BA41-0105XA
0003	01/01/2008		
0004	01/01/2008		
0005	01/01/2008		
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0041	01/01/2008		
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0043	01/01/2008		
0044	01/01/2008		
0045	01/01/2008		
0046	01/01/2008		
0047	01/01/2008		
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0050	01/01/2008		
0051	01/01/2008		
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0099	01/01/2008		
0100	01/01/2008		

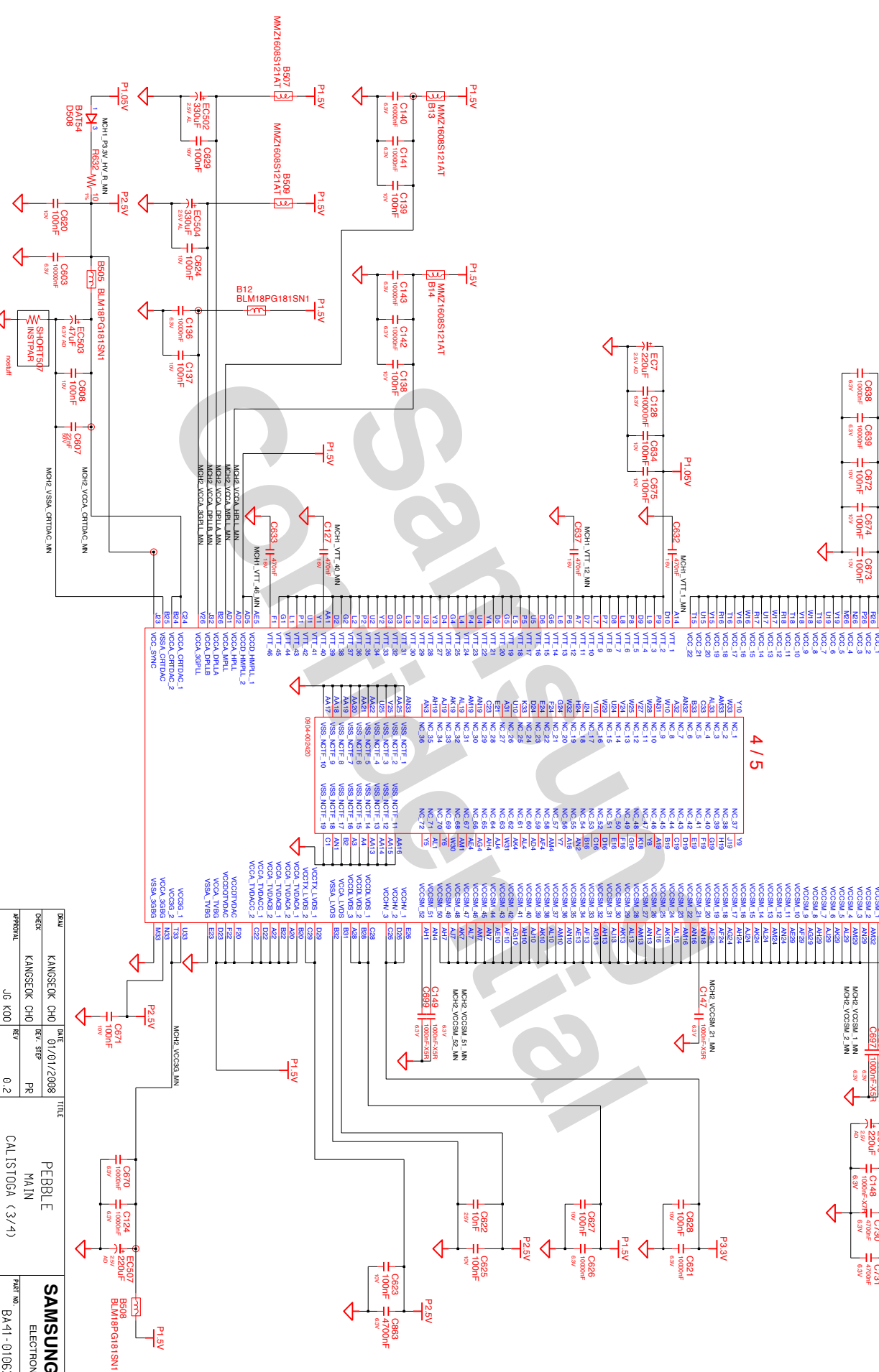




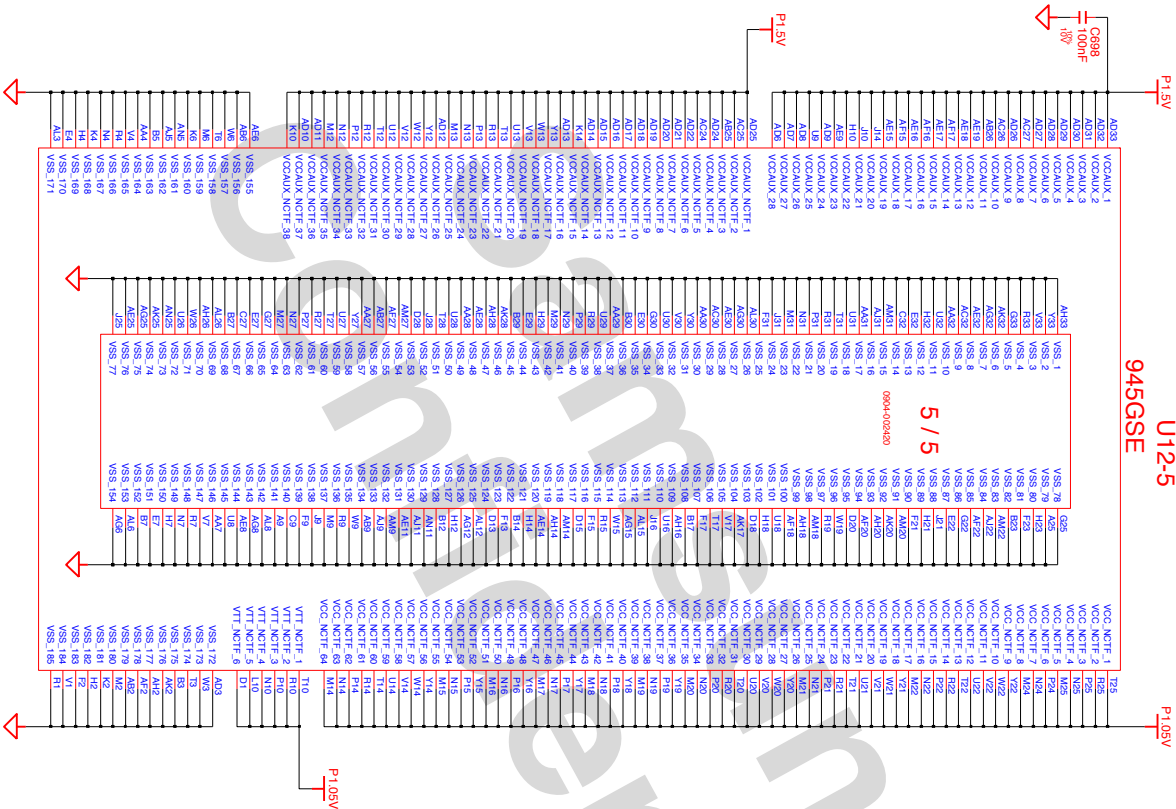
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APPROVAL	REV	0.2		
MODIFY CODE	LAST EDIT	PAGE 13 OF 45		
February 28, 2009 16:53:29 PM				

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U12-4 CALISTOGA (945GSE)
945GSE



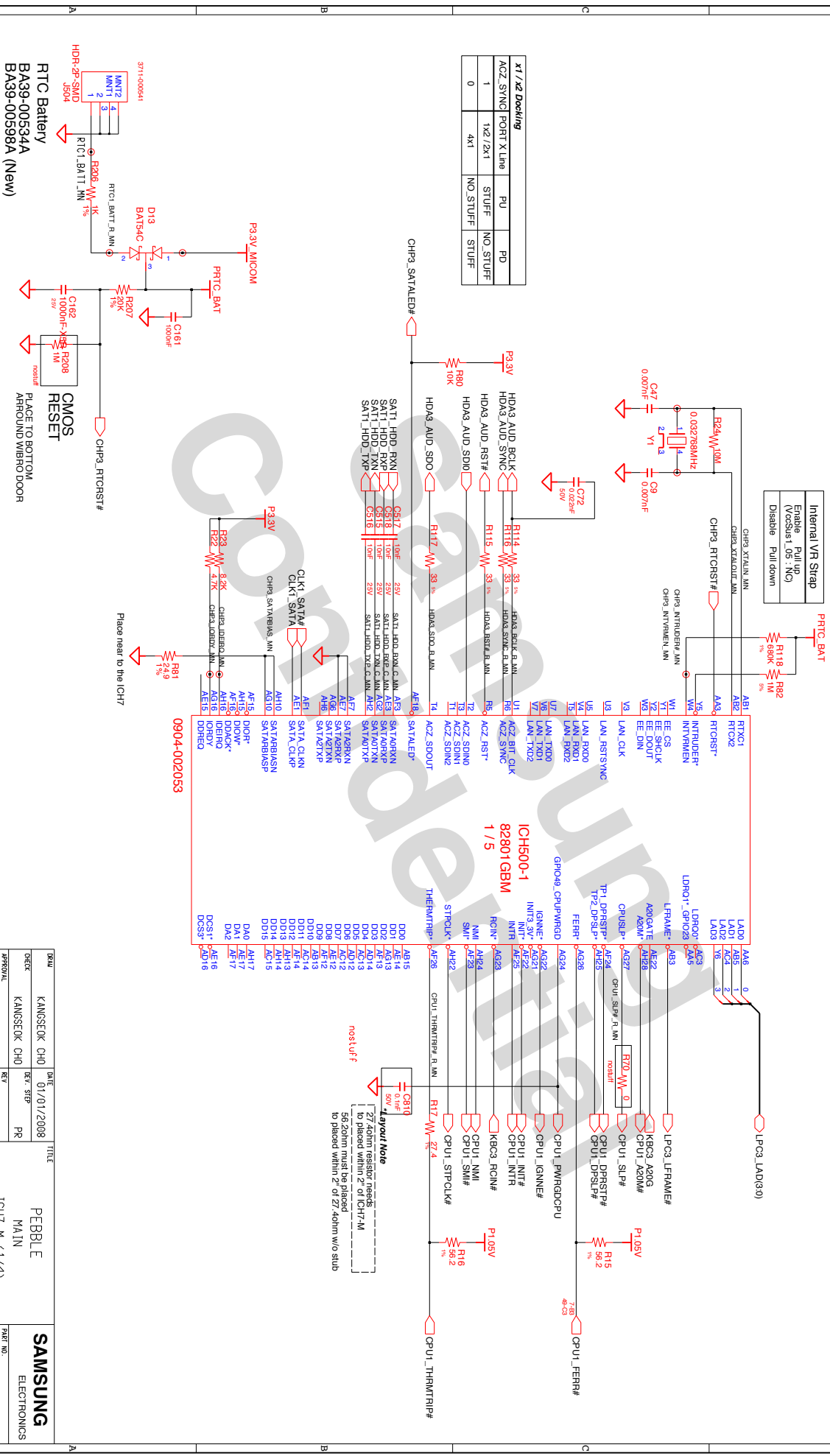
CALISTOGA (945GSE)

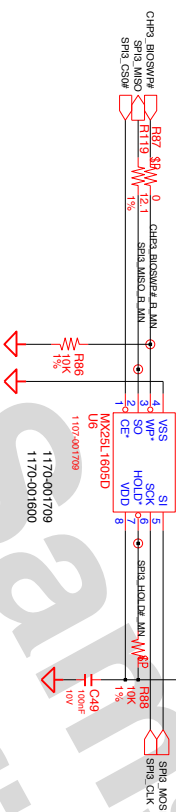


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0.2	PR	MAIN	
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0.2	REV	BA41-0106XA	
0.2	REV	BA41-0106XA	

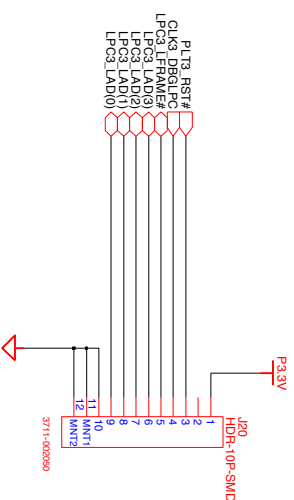
ICH7-M B0 Stepping : 0K65

X1 / X2 Decoding			
ACZ_SYNC	PORT X Line	PU	PD
1	1x2 / 2x1	STUFF	NO_STUFF
0	4x1	NO_STUFF	STUFF





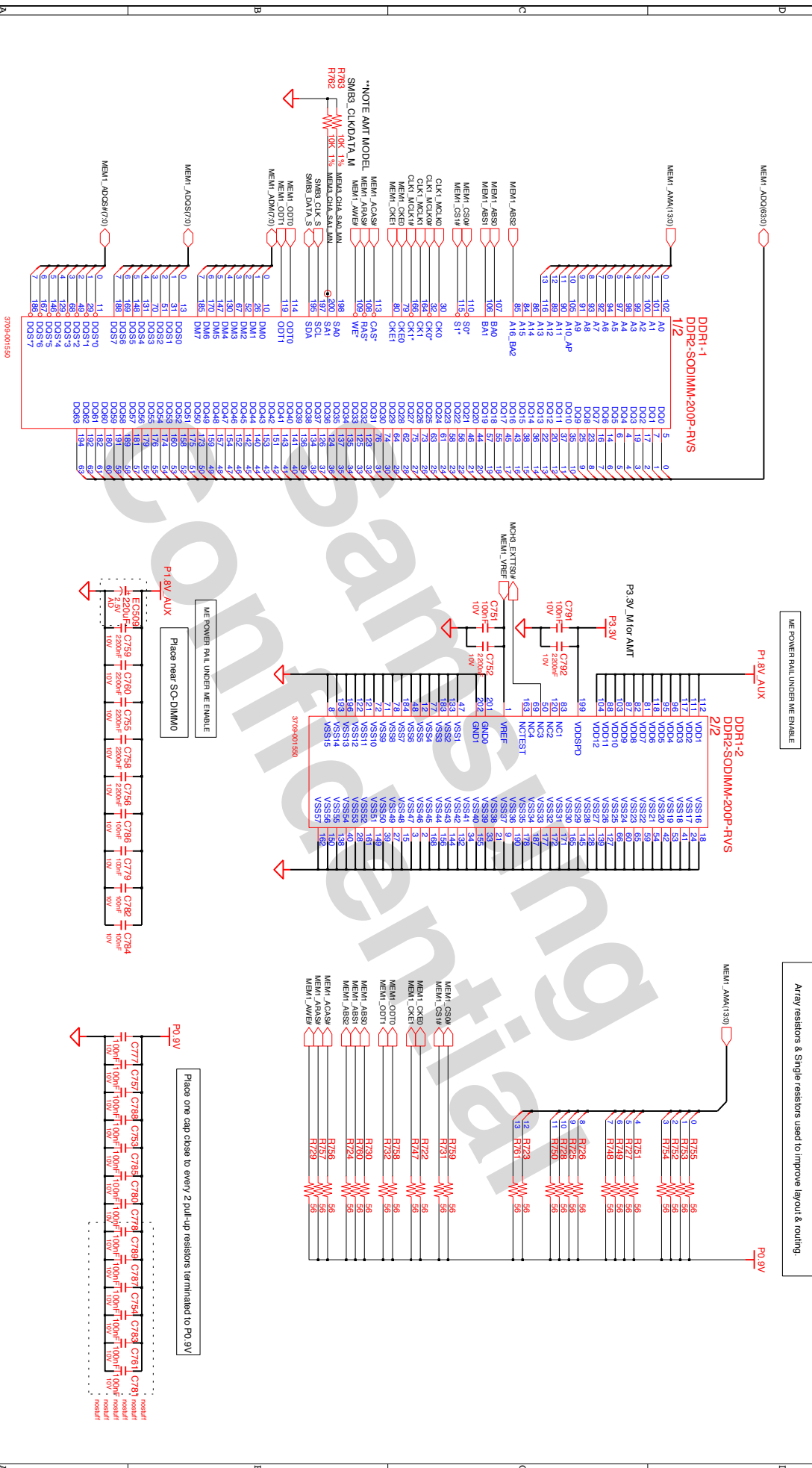
80H DECODER CONNECTOR



04	VERIFY RESET MODE	68	CONFIGURE ADVANCE CACHE REG.
05	DISABLE INIT	69	DISPLAY EXT. MEMORY SIZE
06	GET CPU TYPE	70	DISPLAY SHADOW MESSAGE
07	INIT SYSTEM HW	71	DISPLAY NON-DISPASABLE SEGMENT
08	INIT CHIPSET FLAG	72	DISPLAY ERROR MESSAGE
09	INIT CPU REG	73	SET REAL-TIME CLOCK
0A	INIT CPU REG	74	SET REAL-TIME CLOCK
0B	CPU CACHE ON	75	SETUP FOR KEYBOARD ERROR
0C	INIT CACHE ON POST	76	SECUP FOR KEYBOARD ERROR
0D	ENABLE THE BUS IDE	77	SECUP FOR KEYBOARD ERROR
0E	INIT POWER MANGER	78	DISABLE ONBOARD POST
0F	LOAD A THERMISTOR REG.	79	DETECT AND INSTAL EXT RS232C
10	PCI BUS MASTER HSEL	80	DETECT AND INSTAL EXT RS232C
11	PCI BUS MASTER HSEL	81	REINIT ON-BOARD IO PORT
12	PCI BUS MASTER HSEL	82	REINIT ON-BOARD IO PORT
13	PCI BUS MASTER HSEL	83	REINIT ON-BOARD IO PORT
14	WITH INITIAL POST VALUE	84	REINIT ON-BOARD IO PORT
15	INIT KEYBOARD CONTROLLER	85	INIT EXPANDED BIOS DATA AREA
16	CHECK CHECKSUM	86	INIT FDD CONTROLLER
17	8254 TIMER INIT	87	SHADOW OPTION ROMS
18	8254 TIMER INIT	88	SHADOW OPTION ROMS
19	8254 TIMER INIT	89	SHADOW OPTION ROMS
1A	TEST DIAM REFRESH	90	SETUP POWER MANAGEMENT
1B	TEST DIAM REFRESH	91	SETUP POWER MANAGEMENT
1C	TEST DIAM REFRESH	92	SETUP POWER MANAGEMENT
1D	TEST DIAM REFRESH	93	SETUP POWER MANAGEMENT
1E	TEST DIAM REFRESH	94	SETUP POWER MANAGEMENT
1F	TEST DIAM REFRESH	95	SETUP POWER MANAGEMENT
20	TEST DIAM REFRESH	96	SETUP POWER MANAGEMENT
21	TEST DIAM REFRESH	97	SETUP POWER MANAGEMENT
22	TEST DIAM REFRESH	98	SETUP POWER MANAGEMENT
23	TEST DIAM REFRESH	99	SETUP POWER MANAGEMENT
24	TEST DIAM REFRESH	00	SETUP POWER MANAGEMENT
25	TEST DIAM REFRESH	01	SETUP POWER MANAGEMENT
26	TEST DIAM REFRESH	02	SETUP POWER MANAGEMENT
27	TEST DIAM REFRESH	03	SETUP POWER MANAGEMENT
28	TEST DIAM REFRESH	04	SETUP POWER MANAGEMENT
29	TEST DIAM REFRESH	05	SETUP POWER MANAGEMENT
2A	TEST DIAM REFRESH	06	SETUP POWER MANAGEMENT
2B	TEST DIAM REFRESH	07	SETUP POWER MANAGEMENT
2C	TEST DIAM REFRESH	08	SETUP POWER MANAGEMENT
2D	TEST DIAM REFRESH	09	SETUP POWER MANAGEMENT
2E	TEST DIAM REFRESH	0A	SETUP POWER MANAGEMENT
2F	TEST DIAM REFRESH	0B	SETUP POWER MANAGEMENT
30	TEST DIAM REFRESH	0C	SETUP POWER MANAGEMENT
31	TEST DIAM REFRESH	0D	SETUP POWER MANAGEMENT
32	TEST DIAM REFRESH	0E	SETUP POWER MANAGEMENT
33	TEST DIAM REFRESH	0F	SETUP POWER MANAGEMENT
34	TEST DIAM REFRESH	10	SETUP POWER MANAGEMENT
35	TEST DIAM REFRESH	11	SETUP POWER MANAGEMENT
36	TEST DIAM REFRESH	12	SETUP POWER MANAGEMENT
37	TEST DIAM REFRESH	13	SETUP POWER MANAGEMENT
38	TEST DIAM REFRESH	14	SETUP POWER MANAGEMENT
39	TEST DIAM REFRESH	15	SETUP POWER MANAGEMENT
3A	TEST DIAM REFRESH	16	SETUP POWER MANAGEMENT
3B	TEST DIAM REFRESH	17	SETUP POWER MANAGEMENT
3C	TEST DIAM REFRESH	18	SETUP POWER MANAGEMENT
3D	TEST DIAM REFRESH	19	SETUP POWER MANAGEMENT
3E	TEST DIAM REFRESH	1A	SETUP POWER MANAGEMENT
3F	TEST DIAM REFRESH	1B	SETUP POWER MANAGEMENT
40	TEST DIAM REFRESH	1C	SETUP POWER MANAGEMENT
41	TEST DIAM REFRESH	1D	SETUP POWER MANAGEMENT
42	TEST DIAM REFRESH	1E	SETUP POWER MANAGEMENT
43	TEST DIAM REFRESH	1F	SETUP POWER MANAGEMENT
44	TEST DIAM REFRESH	20	SETUP POWER MANAGEMENT
45	TEST DIAM REFRESH	21	SETUP POWER MANAGEMENT
46	TEST DIAM REFRESH	22	SETUP POWER MANAGEMENT
47	TEST DIAM REFRESH	23	SETUP POWER MANAGEMENT
48	TEST DIAM REFRESH	24	SETUP POWER MANAGEMENT
49	TEST DIAM REFRESH	25	SETUP POWER MANAGEMENT
4A	TEST DIAM REFRESH	26	SETUP POWER MANAGEMENT
4B	TEST DIAM REFRESH	27	SETUP POWER MANAGEMENT
4C	TEST DIAM REFRESH	28	SETUP POWER MANAGEMENT
4D	TEST DIAM REFRESH	29	SETUP POWER MANAGEMENT
4E	TEST DIAM REFRESH	2A	SETUP POWER MANAGEMENT
4F	TEST DIAM REFRESH	2B	SETUP POWER MANAGEMENT
50	TEST DIAM REFRESH	2C	SETUP POWER MANAGEMENT
51	TEST DIAM REFRESH	2D	SETUP POWER MANAGEMENT
52	TEST DIAM REFRESH	2E	SETUP POWER MANAGEMENT
53	TEST DIAM REFRESH	2F	SETUP POWER MANAGEMENT
54	TEST DIAM REFRESH	30	SETUP POWER MANAGEMENT
55	TEST DIAM REFRESH	31	SETUP POWER MANAGEMENT
56	TEST DIAM REFRESH	32	SETUP POWER MANAGEMENT
57	TEST DIAM REFRESH	33	SETUP POWER MANAGEMENT
58	TEST DIAM REFRESH	34	SETUP POWER MANAGEMENT
59	TEST DIAM REFRESH	35	SETUP POWER MANAGEMENT
5A	TEST DIAM REFRESH	36	SETUP POWER MANAGEMENT
5B	TEST DIAM REFRESH	37	SETUP POWER MANAGEMENT
5C	TEST DIAM REFRESH	38	SETUP POWER MANAGEMENT
5D	TEST DIAM REFRESH	39	SETUP POWER MANAGEMENT
5E	TEST DIAM REFRESH	3A	SETUP POWER MANAGEMENT
5F	TEST DIAM REFRESH	3B	SETUP POWER MANAGEMENT
60	TEST DIAM REFRESH	3C	SETUP POWER MANAGEMENT
61	TEST DIAM REFRESH	3D	SETUP POWER MANAGEMENT
62	TEST DIAM REFRESH	3E	SETUP POWER MANAGEMENT
63	TEST DIAM REFRESH	3F	SETUP POWER MANAGEMENT
64	TEST DIAM REFRESH	40	SETUP POWER MANAGEMENT
65	TEST DIAM REFRESH	41	SETUP POWER MANAGEMENT
66	TEST DIAM REFRESH	42	SETUP POWER MANAGEMENT
67	TEST DIAM REFRESH	43	SETUP POWER MANAGEMENT
68	TEST DIAM REFRESH	44	SETUP POWER MANAGEMENT
69	TEST DIAM REFRESH	45	SETUP POWER MANAGEMENT
70	TEST DIAM REFRESH	46	SETUP POWER MANAGEMENT
71	TEST DIAM REFRESH	47	SETUP POWER MANAGEMENT
72	TEST DIAM REFRESH	48	SETUP POWER MANAGEMENT
73	TEST DIAM REFRESH	49	SETUP POWER MANAGEMENT
74	TEST DIAM REFRESH	4A	SETUP POWER

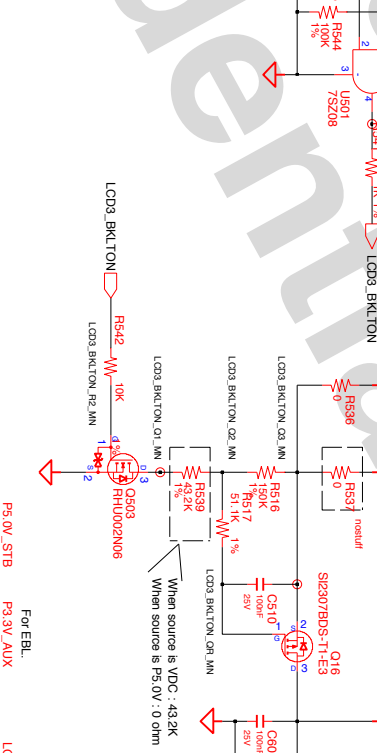
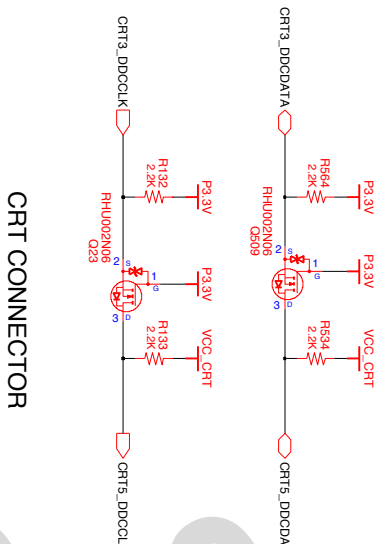
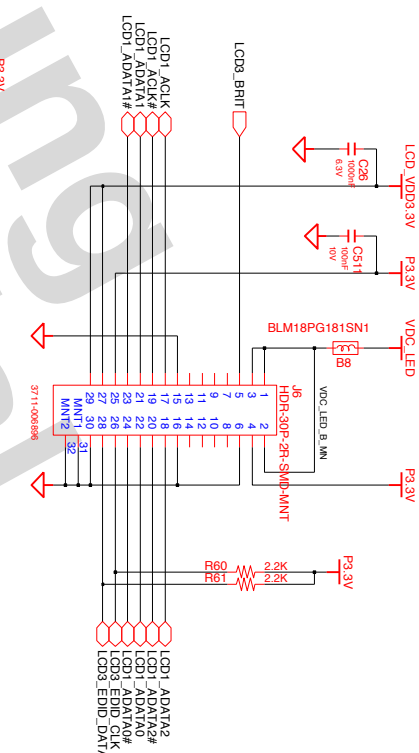
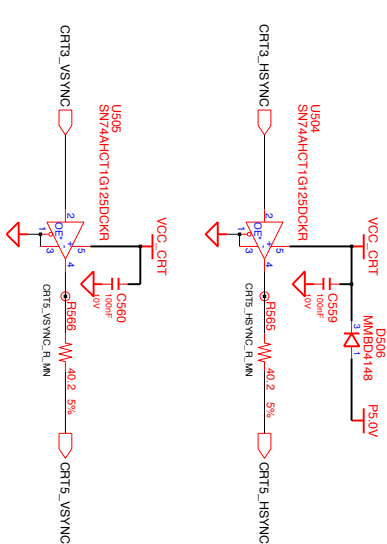
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01/01/2008	PEBLE	ELECTRONICS	
REV: STEP	MAIN		
APPROVAL	REV	PART NO. BA-11-0106XA	
JG KOO	0.2		
USER EDIT	February 28, 2009 16:53:29 PM	PAGE	20 OF 45

DDR SO-DIMM #0

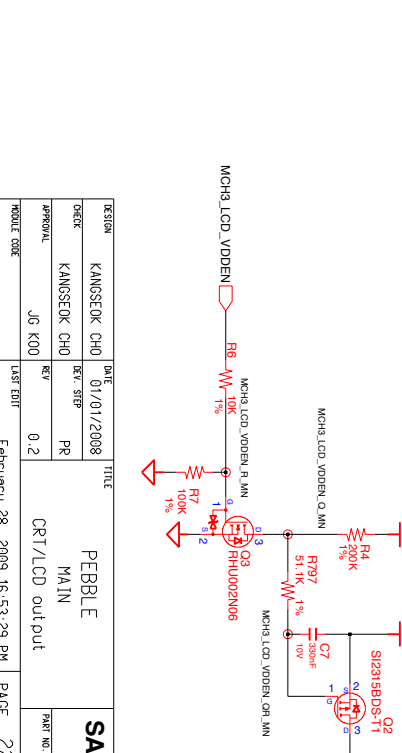
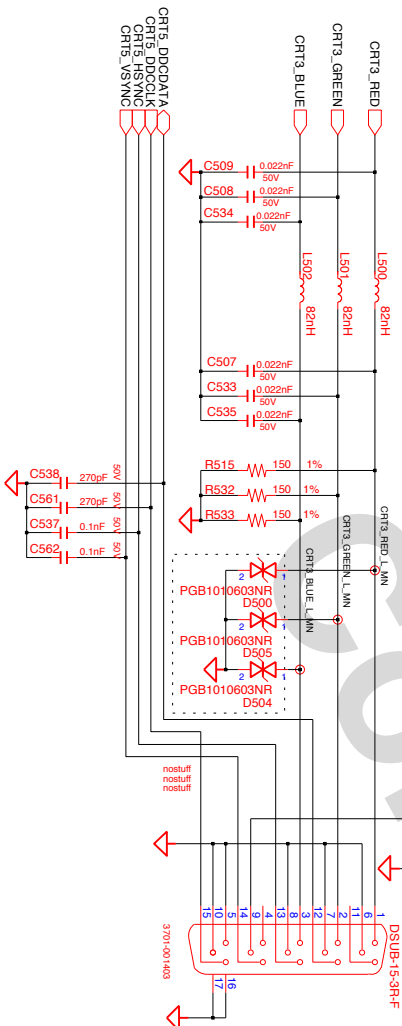


CRT

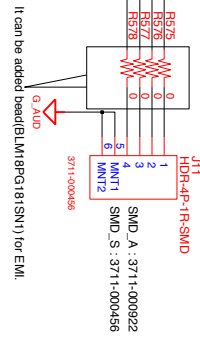
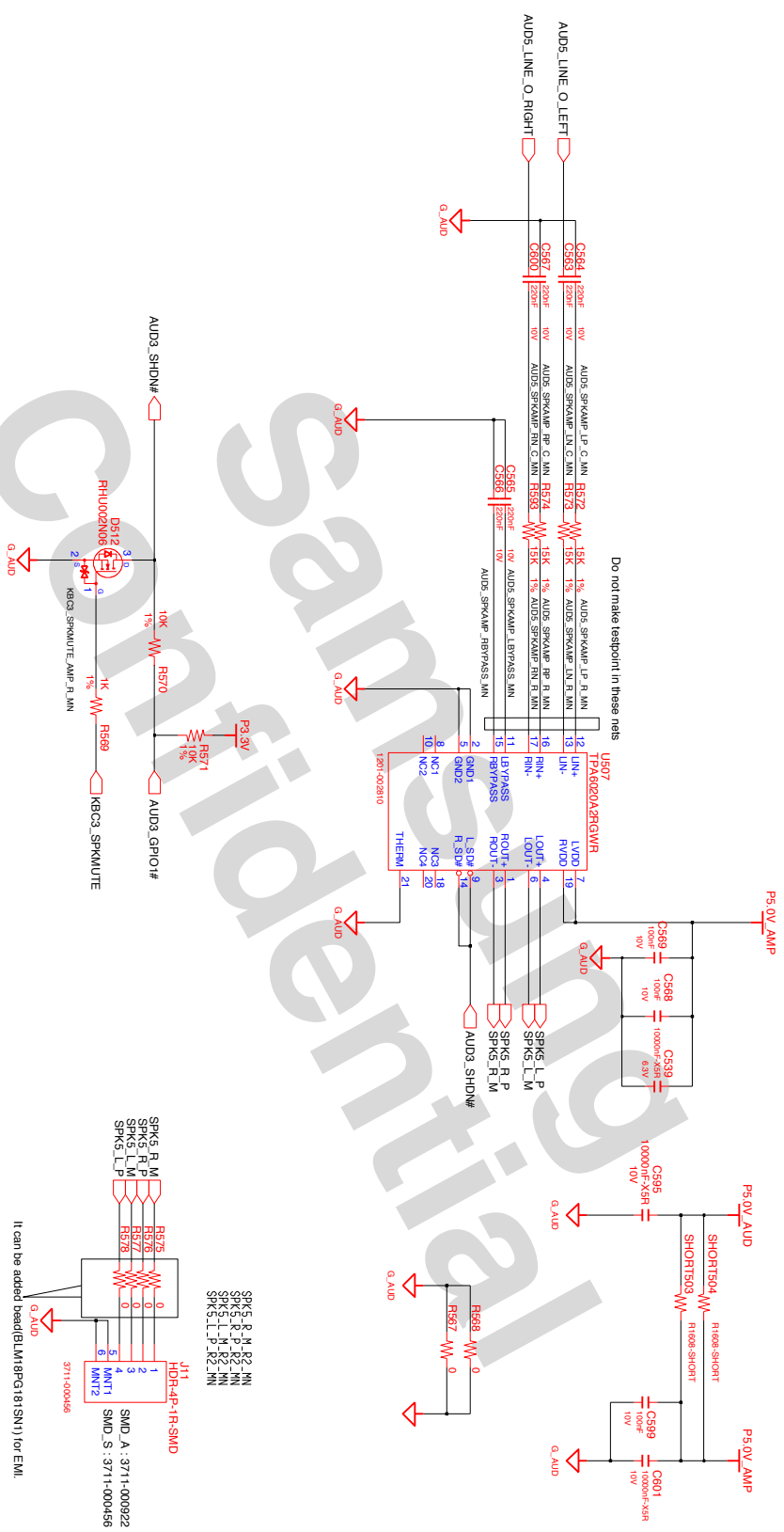
LCD



CRT CONNECTOR

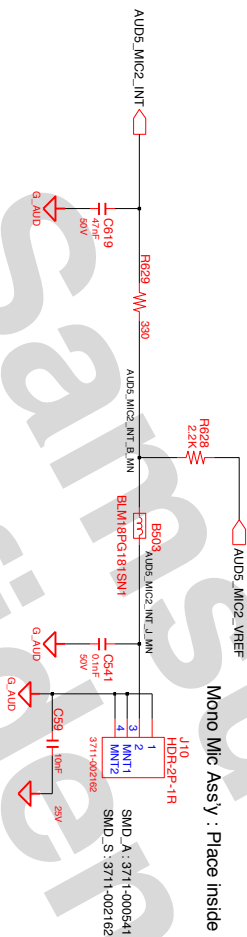


DESIGN	DATE	TITLE	SAMSUNG
KANGSEOK CHO	01/01/2008	PEBBLE	ELECTRONICS
CHECK	REV. STEP	MAIN	
APPROVAL	PR	CRT/LCD output	
REV	0.2		
ROUTE CODE	LCF EDIT	Feb:uary 28, 2009 16:53:29 PM	PAGE 22 OF 45



It can be added bead(BLM18PG181SN1) for EMI.

DESIGN	DATE	TITLE	SAMSUNG
KANGSEOK CHO	01/01/2008	PEBBLE	ELECTRONICS
CHECK	REV. STEP	MAIN	
APPROVAL	PR	AUDIO AMP	
REV	0.2		
ROUTE CODE	LSF EDIT	Feb:var 28, 2009 16:53:29 PM	PAGE 24 OF 45

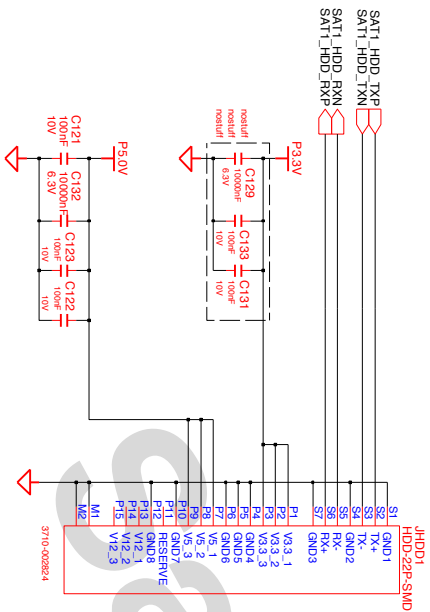


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DESIGN	KANGSEOK_CH0	01/01/2008	PEBBLE		ELECTRONICS	
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APPROVAL	JG_K00	PR	AUD10 internal MIC		BA-41 - 0105XA	
ROUTE CODE		REV	0.2			
LWS EDIT		February 28, 2009 16:53:29 PM	PAGE		25	OF 45

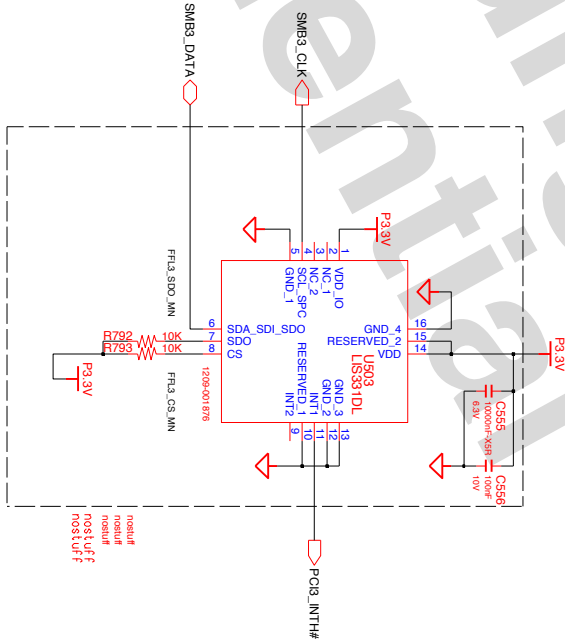
COM-22C-015(1996.6.5) REV. 3

SATA I/F CONN

SATA HDD CONN

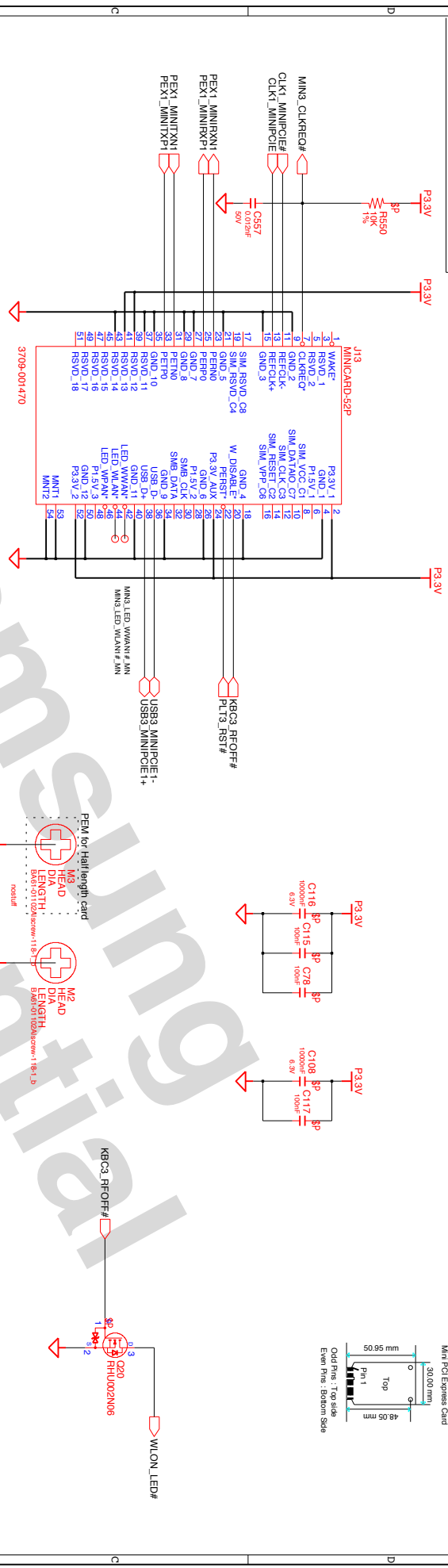


FREE FALL SENSOR

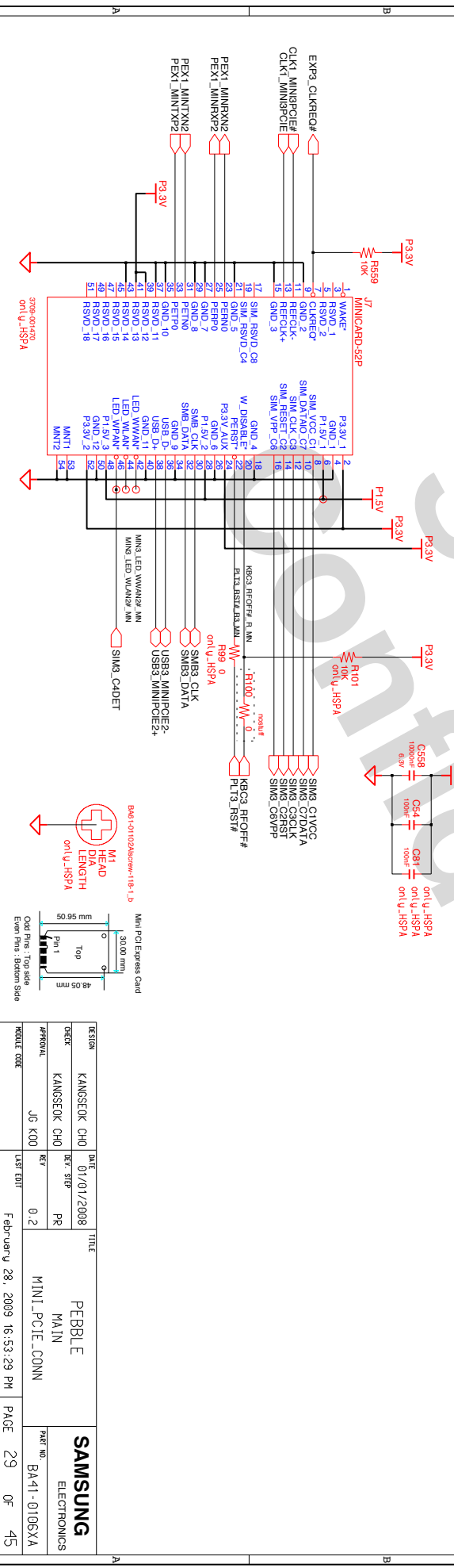


DESIGN	DATE	TITLE	SAMSUNG
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CHK	REV. STEP	MAIN	
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APPROVAL	REV		PART NO.
JG K00	0.2		BA-41-0106XA
ROUTE CODE	LAST EDIT	February 28, 2009 16:53:29 PM	PAGE 28 OF 45

WLAN, 5.2mm



HSDPA / WMBRO, 5.2mm

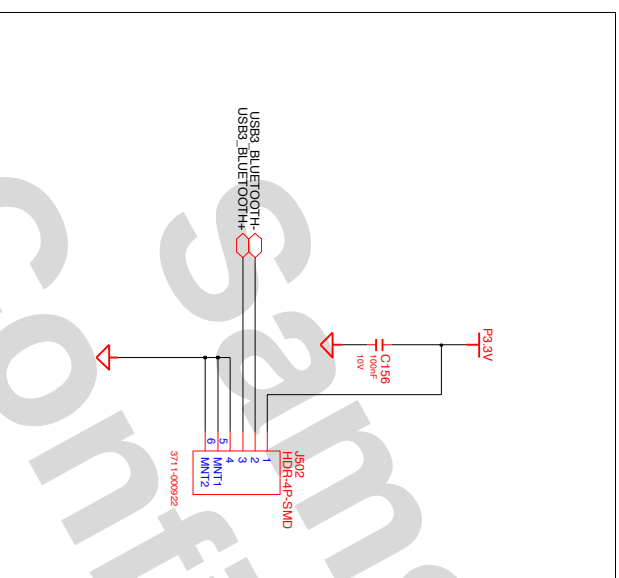


DESIGN	DATE	TITLE	SAMSUNG
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DESIGN	REV. 001	MAIN	
DESIGN	REV. 002	MINI_PCIE_CONN	
DESIGN	REV. 003	REV. 001	
DESIGN	REV. 004	REV. 002	
DESIGN	REV. 005	REV. 003	
DESIGN	REV. 006	REV. 004	
DESIGN	REV. 007	REV. 005	
DESIGN	REV. 008	REV. 006	
DESIGN	REV. 009	REV. 007	
DESIGN	REV. 010	REV. 008	
DESIGN	REV. 011	REV. 009	
DESIGN	REV. 012	REV. 010	
DESIGN	REV. 013	REV. 011	
DESIGN	REV. 014	REV. 012	
DESIGN	REV. 015	REV. 013	
DESIGN	REV. 016	REV. 014	
DESIGN	REV. 017	REV. 015	
DESIGN	REV. 018	REV. 016	
DESIGN	REV. 019	REV. 017	
DESIGN	REV. 020	REV. 018	
DESIGN	REV. 021	REV. 019	
DESIGN	REV. 022	REV. 020	
DESIGN	REV. 023	REV. 021	
DESIGN	REV. 024	REV. 022	
DESIGN	REV. 025	REV. 023	
DESIGN	REV. 026	REV. 024	
DESIGN	REV. 027	REV. 025	
DESIGN	REV. 028	REV. 026	
DESIGN	REV. 029	REV. 027	
DESIGN	REV. 030	REV. 028	
DESIGN	REV. 031	REV. 029	
DESIGN	REV. 032	REV. 030	
DESIGN	REV. 033	REV. 031	
DESIGN	REV. 034	REV. 032	
DESIGN	REV. 035	REV. 033	
DESIGN	REV. 036	REV. 034	
DESIGN	REV. 037	REV. 035	
DESIGN	REV. 038	REV. 036	
DESIGN	REV. 039	REV. 037	
DESIGN	REV. 040	REV. 038	
DESIGN	REV. 041	REV. 039	
DESIGN	REV. 042	REV. 040	
DESIGN	REV. 043	REV. 041	
DESIGN	REV. 044	REV. 042	
DESIGN	REV. 045	REV. 043	
DESIGN	REV. 046	REV. 044	
DESIGN	REV. 047	REV. 045	
DESIGN	REV. 048	REV. 046	
DESIGN	REV. 049	REV. 047	
DESIGN	REV. 050	REV. 048	
DESIGN	REV. 051	REV. 049	
DESIGN	REV. 052	REV. 050	
DESIGN	REV. 053	REV. 051	
DESIGN	REV. 054	REV. 052	
DESIGN	REV. 055	REV. 053	
DESIGN	REV. 056	REV. 054	
DESIGN	REV. 057	REV. 055	
DESIGN	REV. 058	REV. 056	
DESIGN	REV. 059	REV. 057	
DESIGN	REV. 060	REV. 058	
DESIGN	REV. 061	REV. 059	
DESIGN	REV. 062	REV. 060	
DESIGN	REV. 063	REV. 061	
DESIGN	REV. 064	REV. 062	
DESIGN	REV. 065	REV. 063	
DESIGN	REV. 066	REV. 064	
DESIGN	REV. 067	REV. 065	
DESIGN	REV. 068	REV. 066	
DESIGN	REV. 069	REV. 067	
DESIGN	REV. 070	REV. 068	
DESIGN	REV. 071	REV. 069	
DESIGN	REV. 072	REV. 070	
DESIGN	REV. 073	REV. 071	
DESIGN	REV. 074	REV. 072	
DESIGN	REV. 075	REV. 073	
DESIGN	REV. 076	REV. 074	
DESIGN	REV. 077	REV. 075	
DESIGN	REV. 078	REV. 076	
DESIGN	REV. 079	REV. 077	
DESIGN	REV. 080	REV. 078	
DESIGN	REV. 081	REV. 079	
DESIGN	REV. 082	REV. 080	
DESIGN	REV. 083	REV. 081	
DESIGN	REV. 084	REV. 082	
DESIGN	REV. 085	REV. 083	
DESIGN	REV. 086	REV. 084	
DESIGN	REV. 087	REV. 085	
DESIGN	REV. 088	REV. 086	
DESIGN	REV. 089	REV. 087	
DESIGN	REV. 090	REV. 088	
DESIGN	REV. 091	REV. 089	
DESIGN	REV. 092	REV. 090	
DESIGN	REV. 093	REV. 091	
DESIGN	REV. 094	REV. 092	
DESIGN	REV. 095	REV. 093	
DESIGN	REV. 096	REV. 094	
DESIGN	REV. 097	REV. 095	
DESIGN	REV. 098	REV. 096	
DESIGN	REV. 099	REV. 097	
DESIGN	REV. 100	REV. 098	

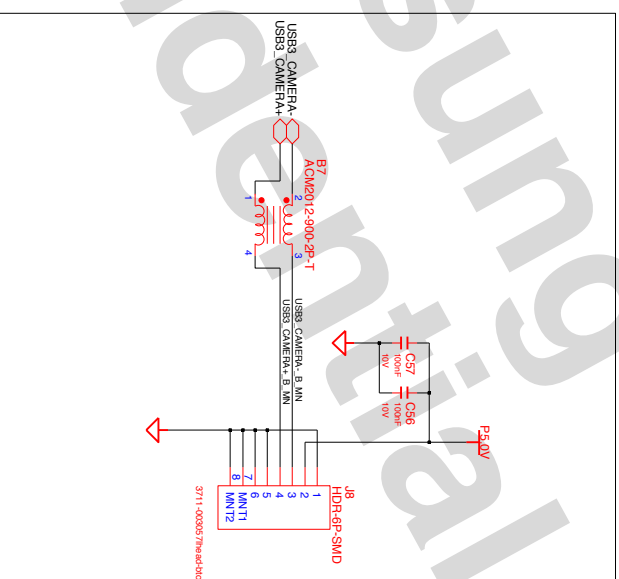


DESIGN	KANSEOK CH0	DATE	01/01/2008	TITLE	PERBLE MAIN USB CONN	SAMSUNG ELECTRONICS		
CHECK	KANSEOK CH0	REV. STEP	PR			PART NO.	BA-41-0105XA	
APPROVAL	JG K00	REV.	0.2					
MODIFY CODE		LIST EDIT		February 28, 2009 16:53:29 PM	PAGE	30	OF	45

Bluetooth Interface

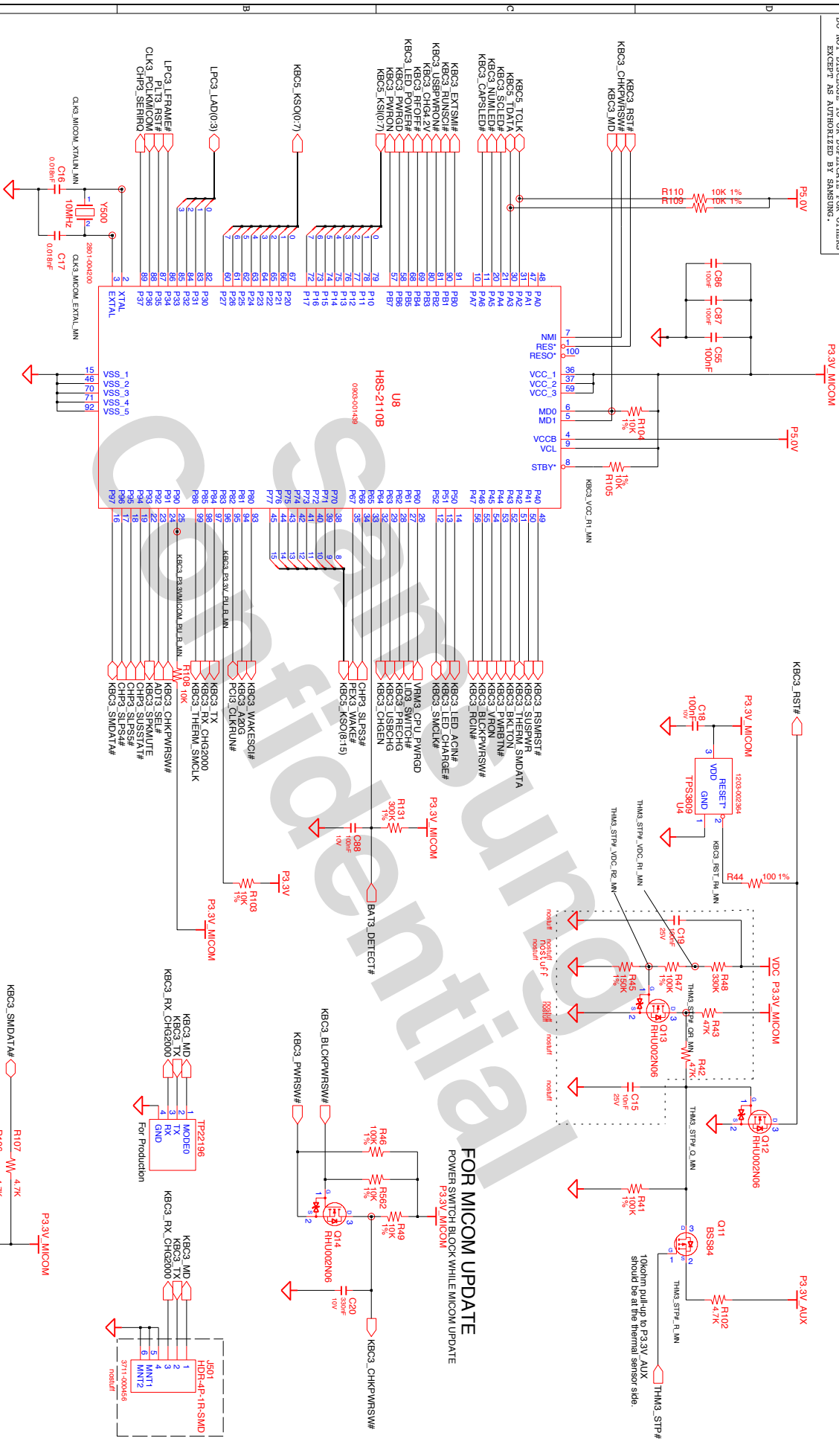


CAMERA



DESIGN	DATE	TITLE	SAMSUNG	
KANSEOK CH0	01/01/2008	PEBBLE	ELECTRONICS	
ORDER	REV. STEP	MAIN		
KANSEOK CH0	PR	USB-DEVICES		
APPROVAL	REV	0.2	PART NO. BA-11-0106XA	
MODULE CODE	LAST EDIT	February 28, 2009 16:53:29 PM	PAGE	31 OF 45

MICOM RESET

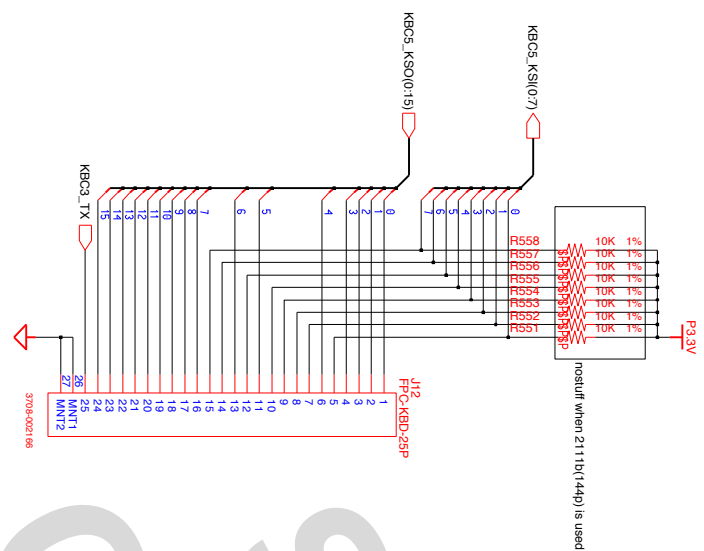


Micom Glue Logic

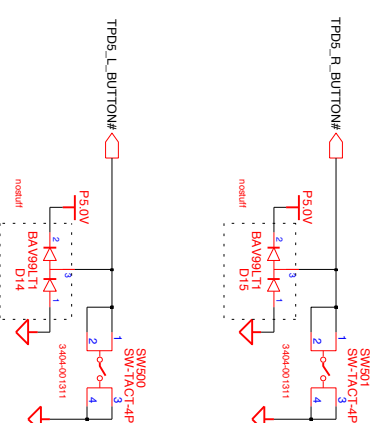
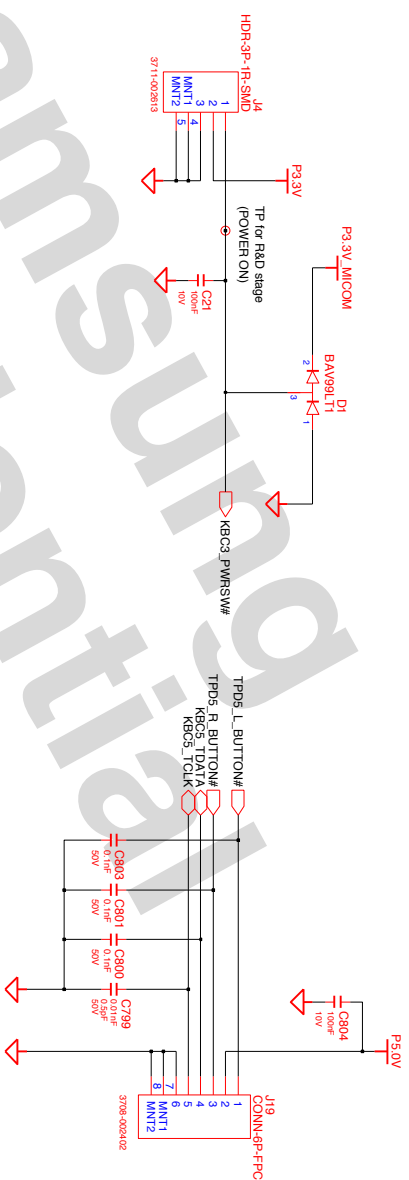
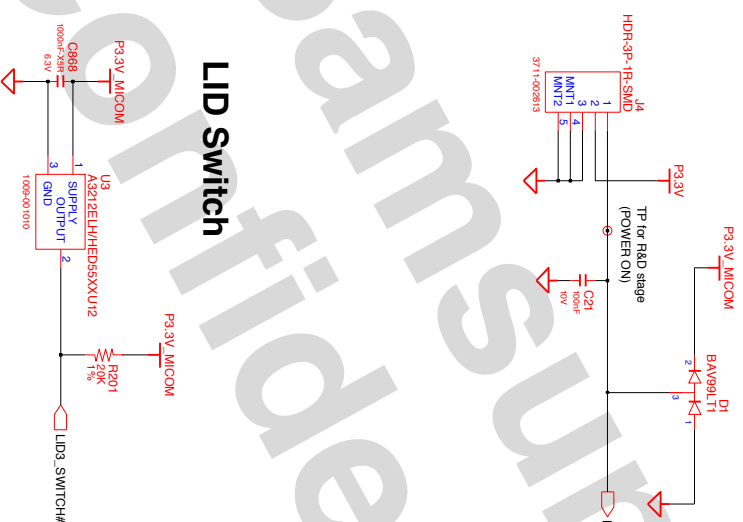
KEYBOARD

Power Switch Button

TOUCHPAD



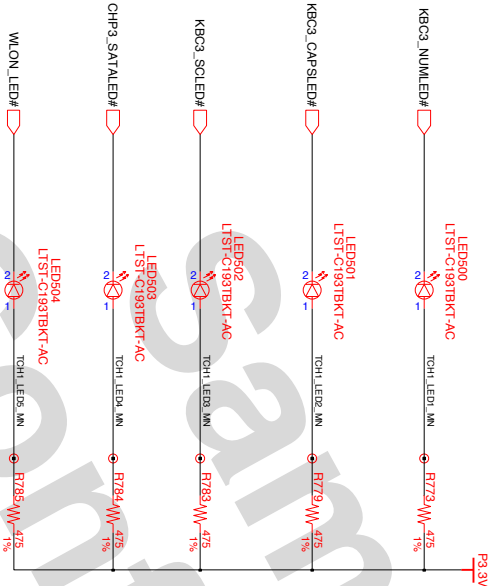
LID Switch



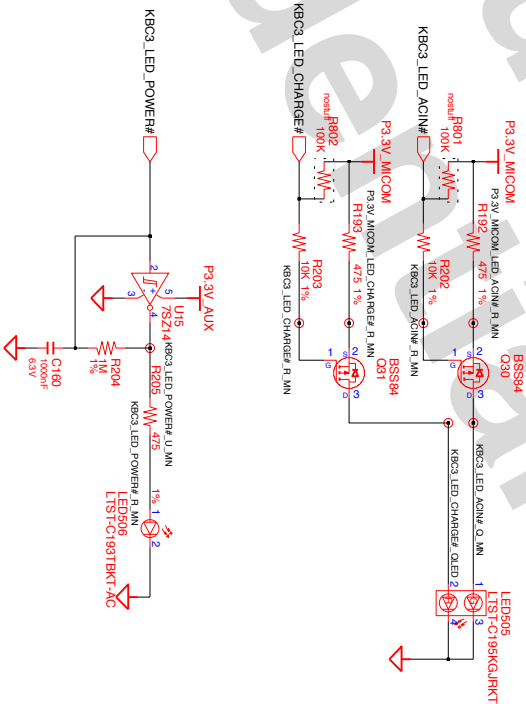
DESIGN	DATE	TITLE	SAMSUNG
KANGSEOK CHO	01/01/2008	PEBBLE	ELECTRONICS
CHK	REV. STEP	MAIN	
KANGSEOK CHO	PR	MICOM GLUE LOGIC	
APPROVAL	REV	0.2	PART NO. BA-41-0105XA
JG KOO	LSF EDIT	February 28, 2009 16:53:29 PM	PAGE 34 OF 45
ROUTE CODE			

LED SWITCH LOGIC

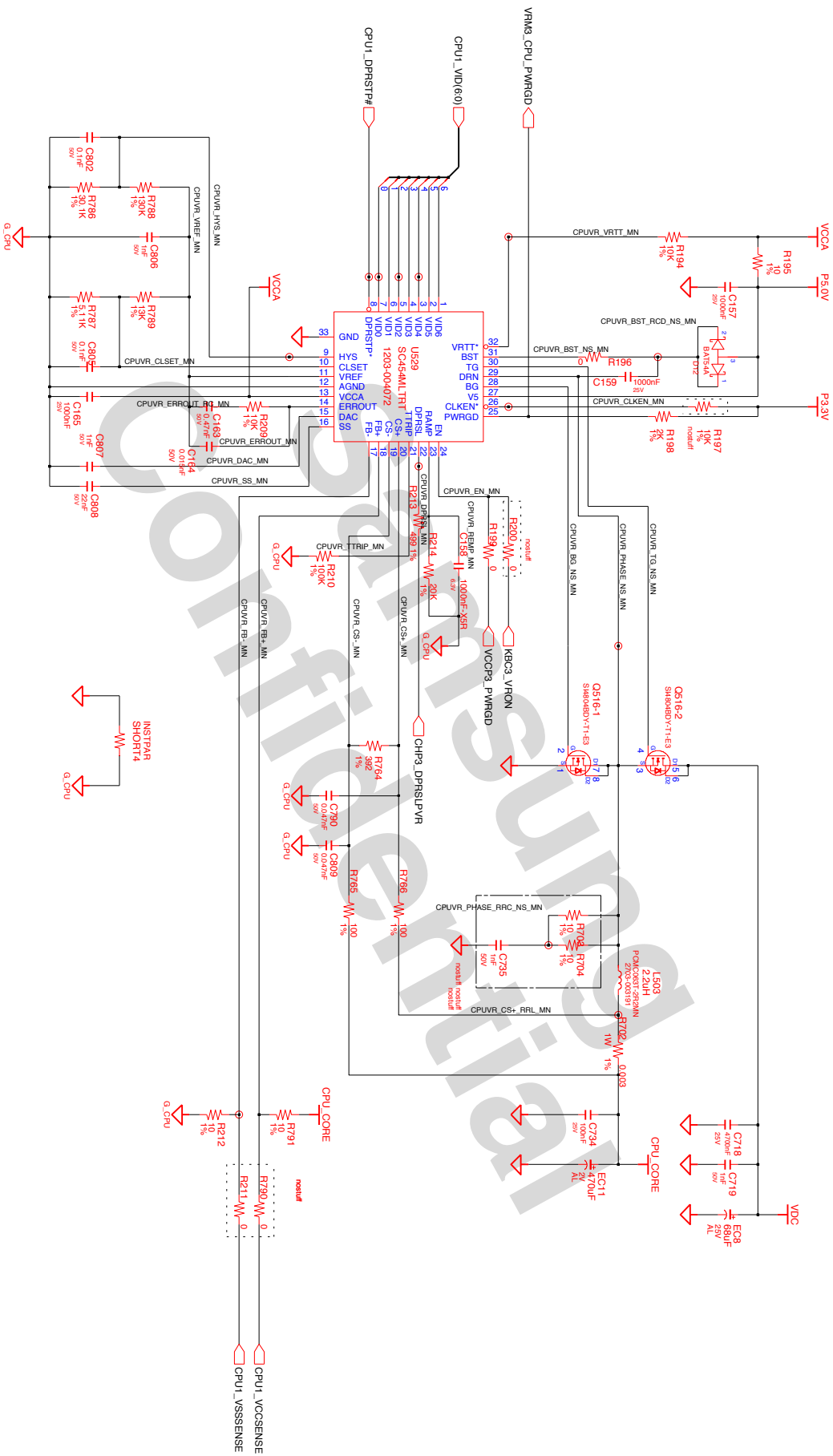
Function Key LEDs




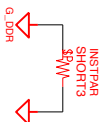
ADAPTERIN/CHARGING LED



REV	DATE	TITLE	SAMSUNG
01/01/2008	01/01/2008	PEBBLE	
02/01/2008	02/01/2008	MAIN	
03/01/2008	03/01/2008	LED-Switch	
04/01/2008	04/01/2008		
05/01/2008	05/01/2008		
06/01/2008	06/01/2008		
07/01/2008	07/01/2008		
08/01/2008	08/01/2008		
09/01/2008	09/01/2008		
10/01/2008	10/01/2008		
11/01/2008	11/01/2008		
12/01/2008	12/01/2008		
13/01/2008	13/01/2008		
14/01/2008	14/01/2008		
15/01/2008	15/01/2008		
16/01/2008	16/01/2008		
17/01/2008	17/01/2008		
18/01/2008	18/01/2008		
19/01/2008	19/01/2008		
20/01/2008	20/01/2008		
21/01/2008	21/01/2008		
22/01/2008	22/01/2008		
23/01/2008	23/01/2008		
24/01/2008	24/01/2008		
25/01/2008	25/01/2008		
26/01/2008	26/01/2008		
27/01/2008	27/01/2008		
28/01/2008	28/01/2008		
29/01/2008	29/01/2008		
30/01/2008	30/01/2008		
31/01/2008	31/01/2008		
32/01/2008	32/01/2008		
33/01/2008	33/01/2008		
34/01/2008	34/01/2008		
35/01/2008	35/01/2008		
36/01/2008	36/01/2008		
37/01/2008	37/01/2008		
38/01/2008	38/01/2008		
39/01/2008	39/01/2008		
40/01/2008	40/01/2008		
41/01/2008	41/01/2008		
42/01/2008	42/01/2008		
43/01/2008	43/01/2008		
44/01/2008	44/01/2008		
45/01/2008	45/01/2008		



DESIGN	KANSELEK CHO	DATE	01/01/2008	TITLE	PEBBLE POWER CPUVRM		
CHECK	KANSELEK CHO	REV. STEP	PR			PART NO.	BA-11-0106XA
APPROVAL	JG KOO	REV	0.2				
MODEL CODE		LAST EDIT	February 28, 2009 16:53:29 PM	PAGE	36	OF	45

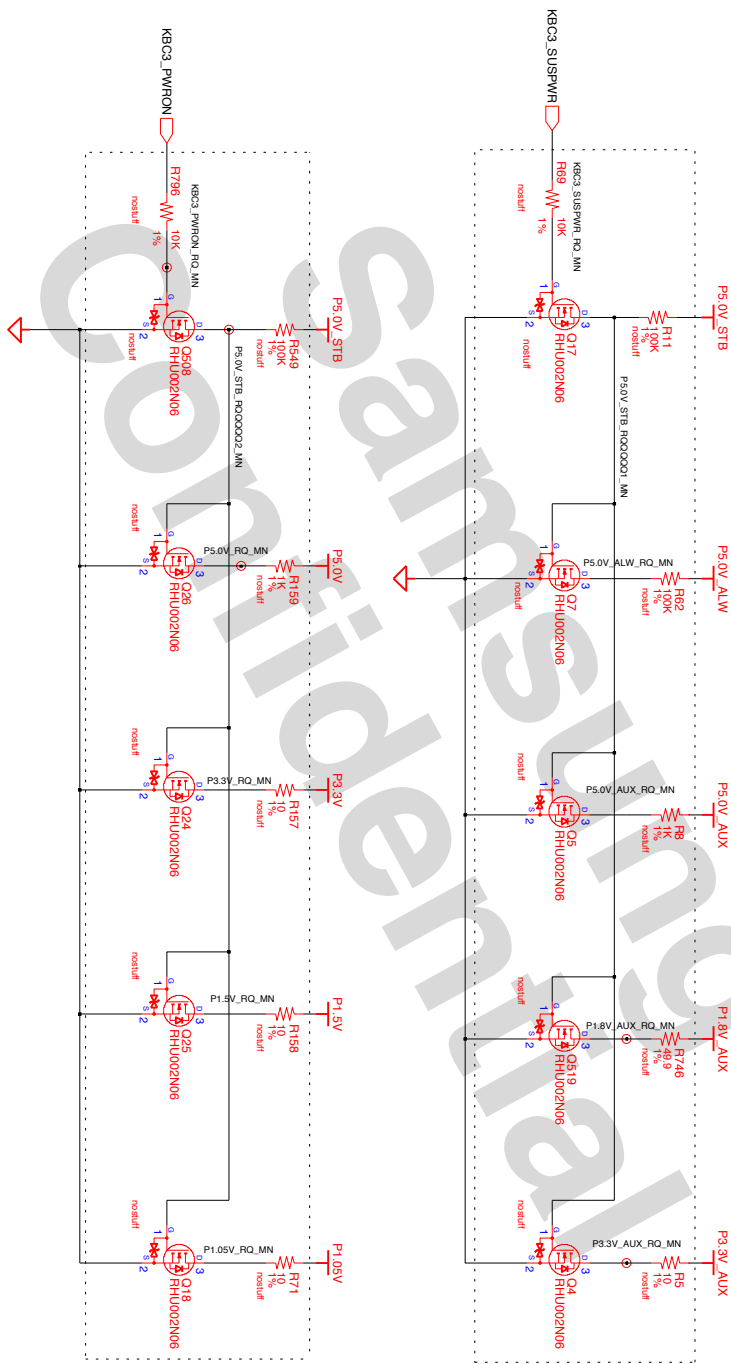


RAW	DATE	TITLE	SAMSUNG ELECTRONICS		
KANOSSEK CHIO	01/01/2008	PEBBLE	PART NO.	BA-41-0106XA	
DECK	REV. STEP	POWER			
KANOSSEK CHIO	PR	DD22 POWER			
APPROVAL	REV	0.2			
MODEL CODE	DATE EDIT	February 28, 2009 16:53:29 PM	PAGE	38	OF 45
	under med				



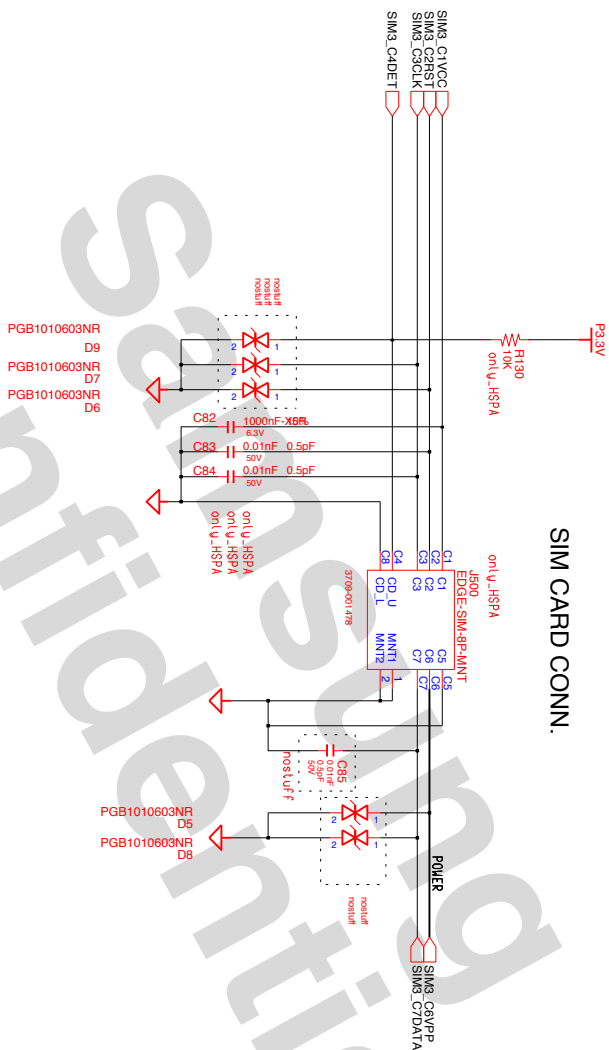
DESIGN	TITLE	SAMSUNG ELECTRONICS	
KANSEOK CH0	07/01/2008		
CHICK	REV. STEP		
KANSEOK CH0	PR		
PERSONAL	REV		
JG K00	0.2		
MODULE CODE	LAST EDIT		
	February 28, 2009 16:53:29 PM		
	PAGE	41	OF 45
		BA-11-0106XA	

POWER DISCHARGER



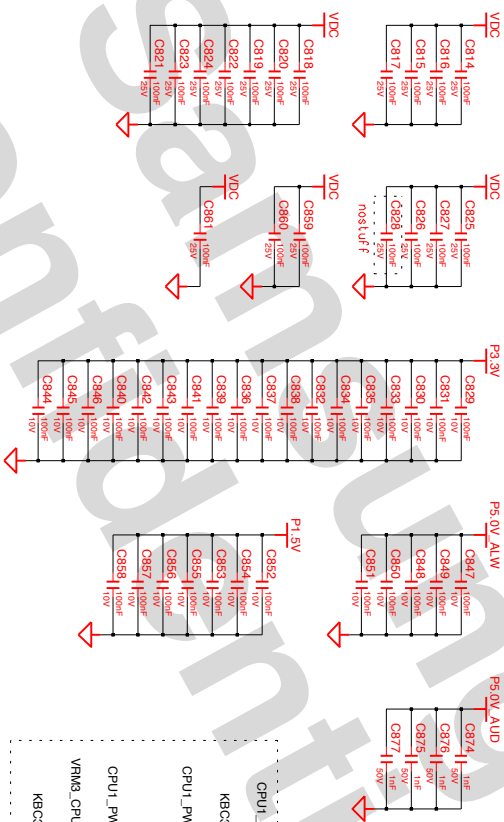
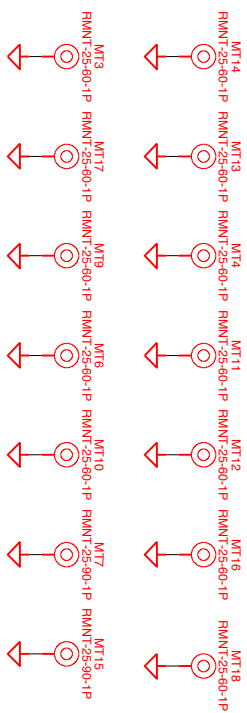
DESIGN	KANGSEOK CHO	DATE	01/01/2008	TITLE	PEBBLE	SAMSUNG
CHECK	KANGSEOK CHO	REV. STEP	PR	POWER	POWER	ELECTRONICS
APPROVAL	JC KOO	REV	0.2	DISCHARGER	DISCHARGER	BA-41-0105XA
ROUTE CODE		LAST EDIT	February 28, 2009 16:53:29 PM	PAGE	42	OF 45

SIM CARD CONN.



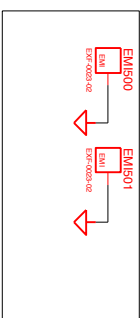
BAW	KANGSEOK CHO	DATE	01/01/2008	TITLE	PEBBLE SUB BOARD	SAMSUNG ELECTRONICS
DECK	KANGSEOK CHO	REV. STEP	PR		SIM CARD	BA-41 - 0105XA
APPROVAL	JC KOO	REV	0.2			
ROUTE CODE		LAST EDIT	February 28, 2009 16:53:29 PM	PAGE	43	OF 45

MOUNT holes



FOR EMI
(When the EMI decap need, they will be updated in this sheet)

EMI CLIP



PCB REVISION CONTROL (ICT)		
NO	CONNECTION DATE(Y/M/M/D)	REVISION STEP
1	N.C.	
2	1-2	
3	2-3	
4	3-1	
5	1-2-3	
6	N.C.	
7	1-2	
8	2-3	
9	3-1	
10	1-2-3	

REV:300
10
20 03

REV	DATE	TITLE	SAMSUNG
BAW	KANGSEOK CH0	01/01/2008	PEBBLE
DECK	KANGSEOK CH0	REV. STEP	MAIN
APPROVAL	JC K00	PR	MOUNT HOLE / EMI
ROUTE CODE	LAST EDIT	February 28, 2009 16:53:29 PM	PAGE 44 OF 45

